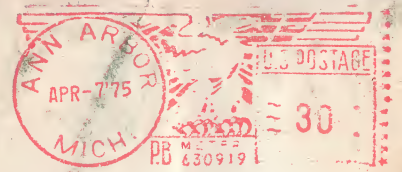




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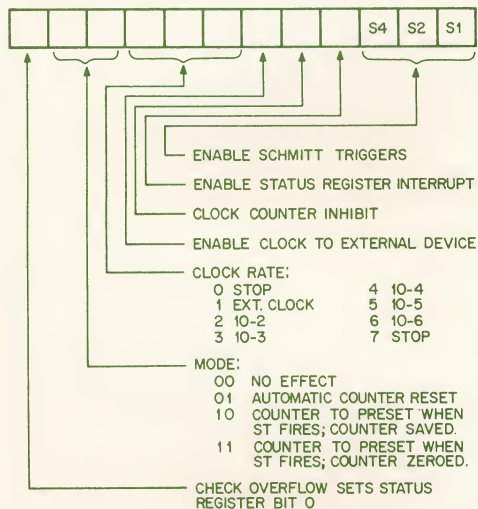
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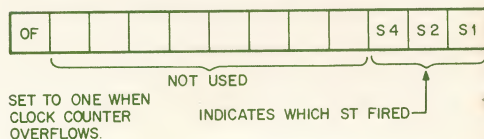
REAL TIME CLOCK (DK8-EP)

CLZE	6130	One's in AC clear clock-enable register
CLSK	6131	Skip if clock overflows or ST set
CLOE	6132	One's in AC set clock-enable register
CLAB	6133	AC to buffer/ preset and counter registers
CLED	6134	Clock-enable to AC
CLSA	6135	Clock status register to AC and AC one's clear status register
CLBA	6136	Clock buffer/preset to AC
CLCA	6137	Counter register to buffer/preset and to AC

CLOCK ENABLE REGISTER

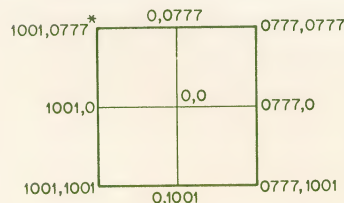


CLOCK STATUS REGISTER



DISPLAY CONTROL (VC8E)

DICL	6050	Clears enables, flags
DICD	6051	Clears done flag
DISD	6052	Skip on display done flag (no clr)
DILX	6053	Load x register
DILY	6054	Load y register
DIXY	6055	Intensify
DILE	6056	Load enable from AC, clear AC
DIRE	6057	Enable to AC



* 10 Bit, 2's complement form

DISPLAY ENABLE REGISTER



0—Display done flag
10—Display Channel
11—Display done causes interrupt

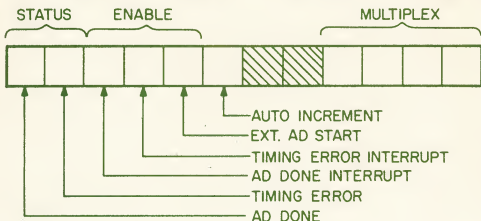
lab8/e

INSTRUCTION LIST

ANALOG TO DIGITAL CONVERTER (AD8-EA/AM8-EA)

ADCL	6530	Clear all
ADLM	6531	Load multiplexer from AC, clear AC
ADST	6532	Start AD conversion, Clear Done
ADRB	6533	Read AD Buffer
ADSK	6534	Skip on AD done
ADSE	6535	Skip on timing error
ADLE	6536	Load AD-enable register, Clear AC
ADRS	6537	Read enable, status and MUX

MULTIPLEX, ENABLE AND STATUS REGISTER



Status register:

- 0—set if AD is done
- 1—timing error flag

Enable register:

- 2—enable interrupt on AD done
- 3—enable interrupt on timing error
- 4—enable ext. AD start (clock)
- 5—auto-increment multiplexer

Multiplex register:

bits 8-11

indicates current AD channel to be sampled
(0-17₈)

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